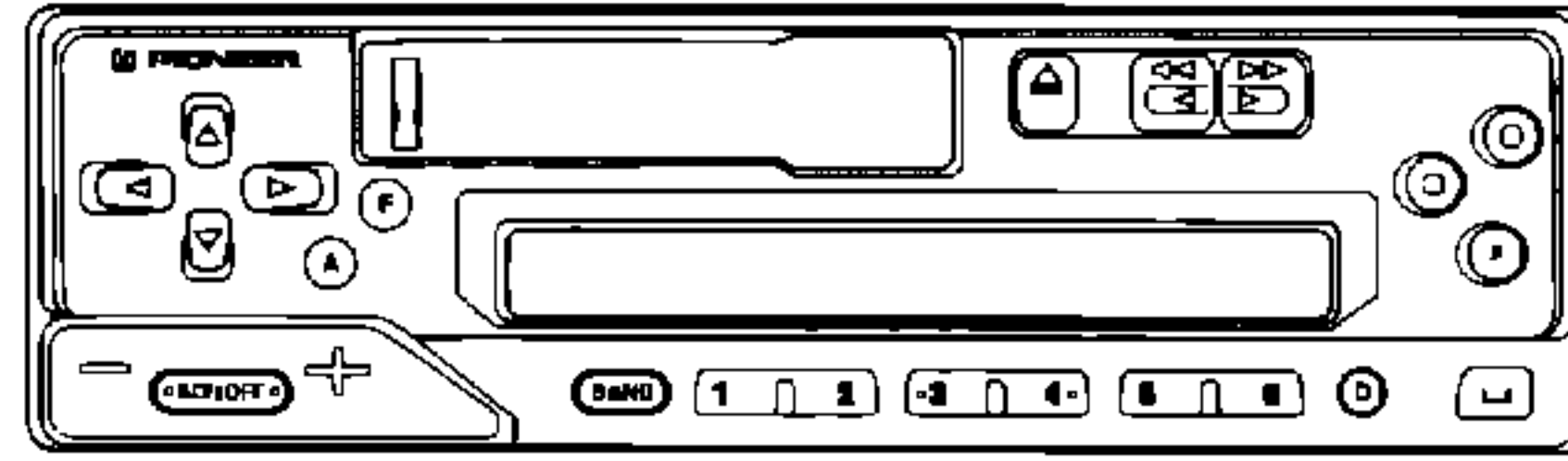


Service Manual

PIONEER®
The Art of Entertainment

• KEH-P3600R/X1M/EW



ORDER NO.
CRT1950

MULTI-CD CONTROL HIGH POWER CASSETTE FM/AM TUNER

KEH-P3600R X1M/EW

KEH-P3630R X1M/EW

NOTE:

- See the separate manual CX-644(CRT1800) for the cassette mechanism description.
- The cassette mechanism assy employed in this model is one of X-2M series
- This model has no CD test mode.

For the operations in the CD test mode, refer to the CD player's Service Manual.

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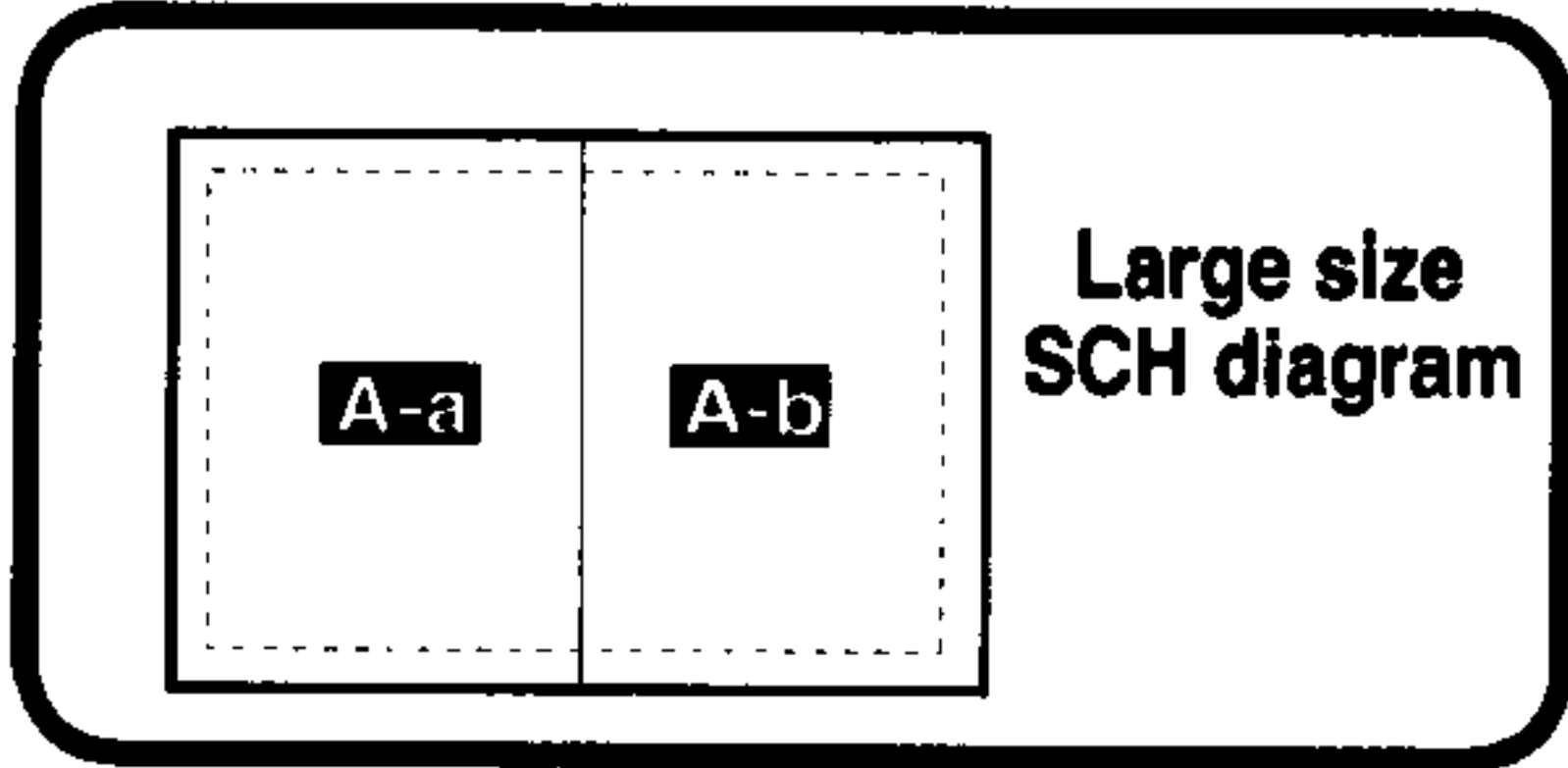
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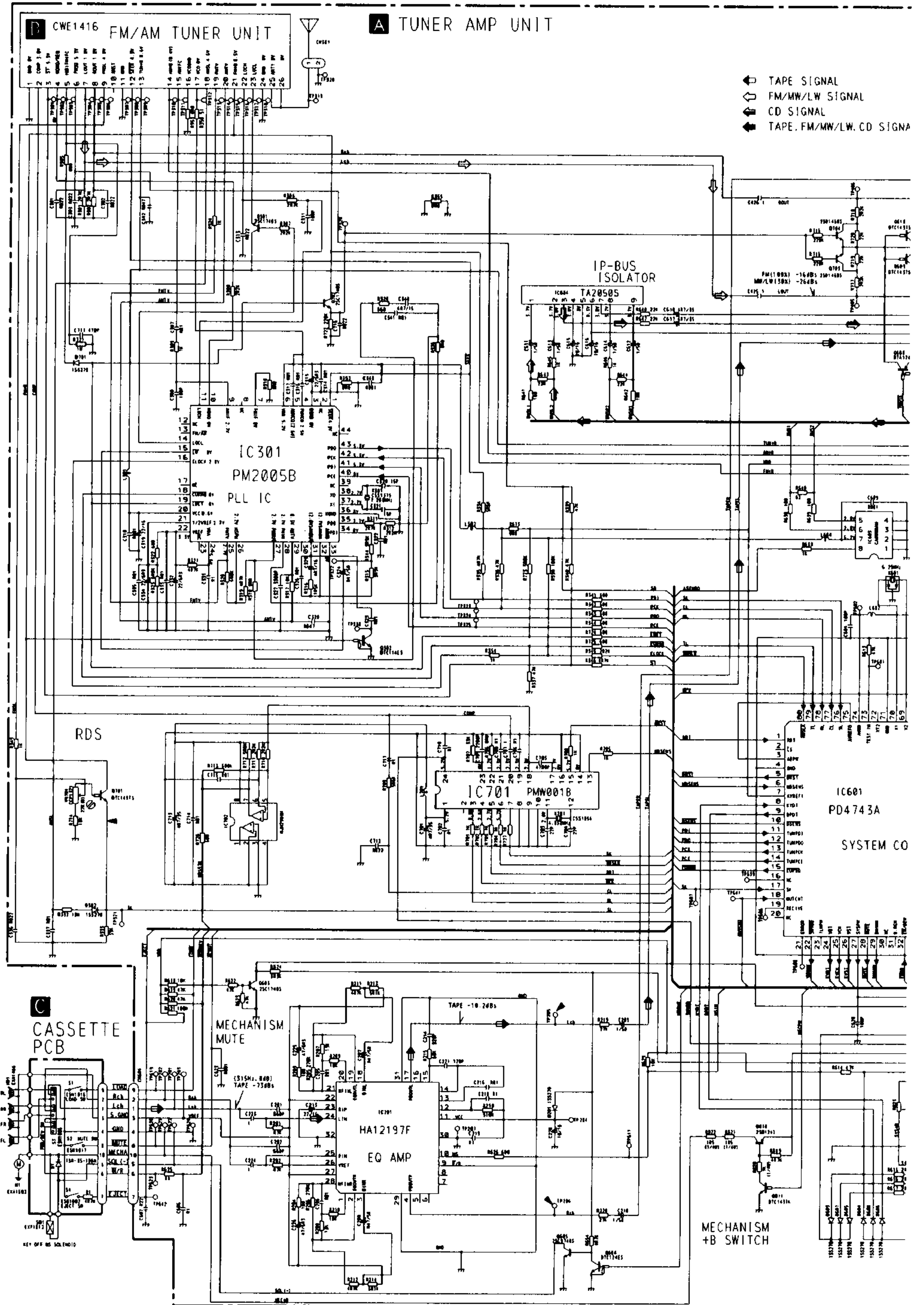
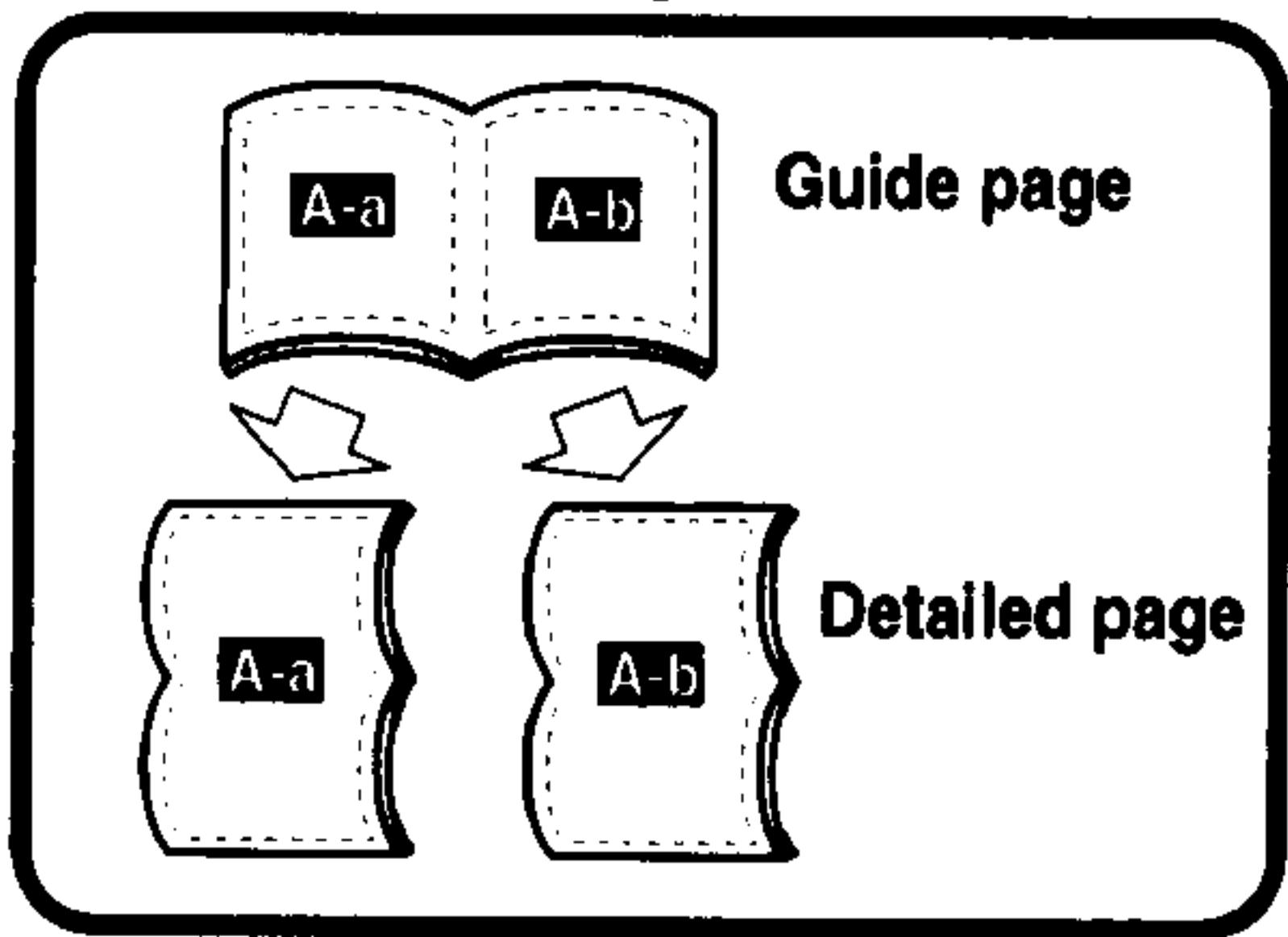
3. SCHEMATIC DIAGRAM

3.1 OVERALL CONNECTION DIAGRAM (GUIDE PAGE)

Note: When ordering service parts, be sure to refer to "EXPLODED VIEWS AND PARTS LIST" or "ELECTRICAL PARTS LIST".



A-a



A-b

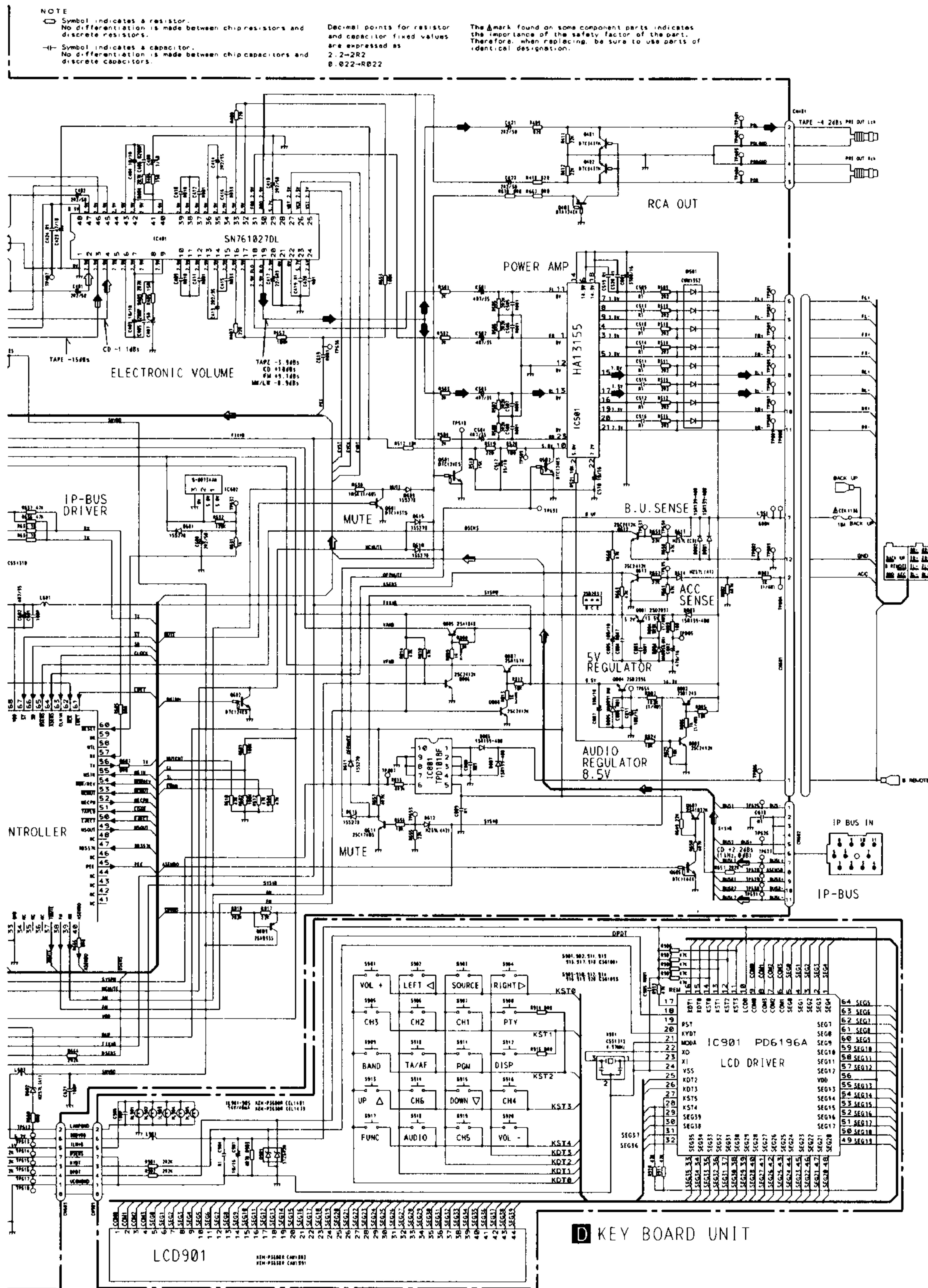


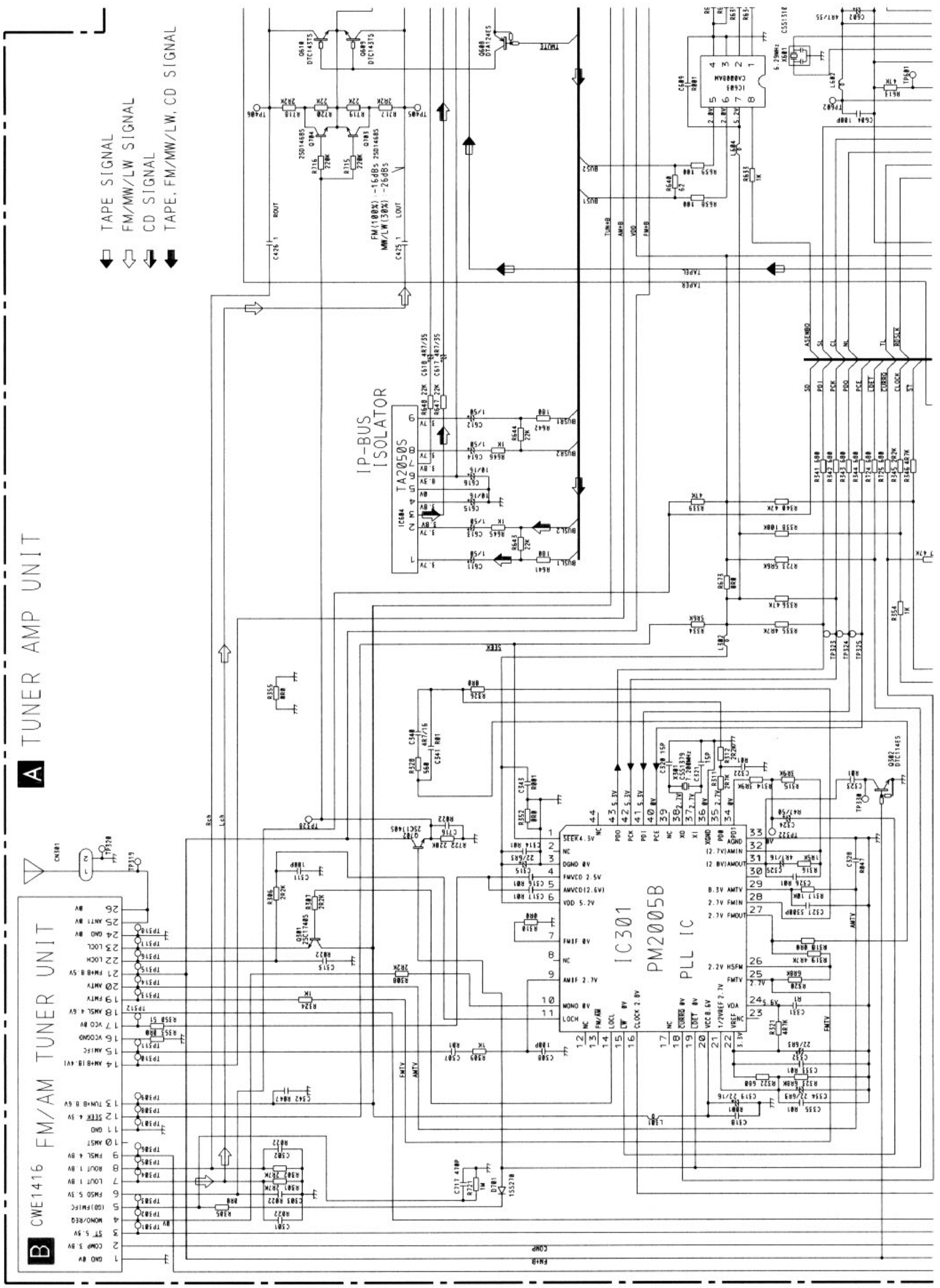
Fig. 5

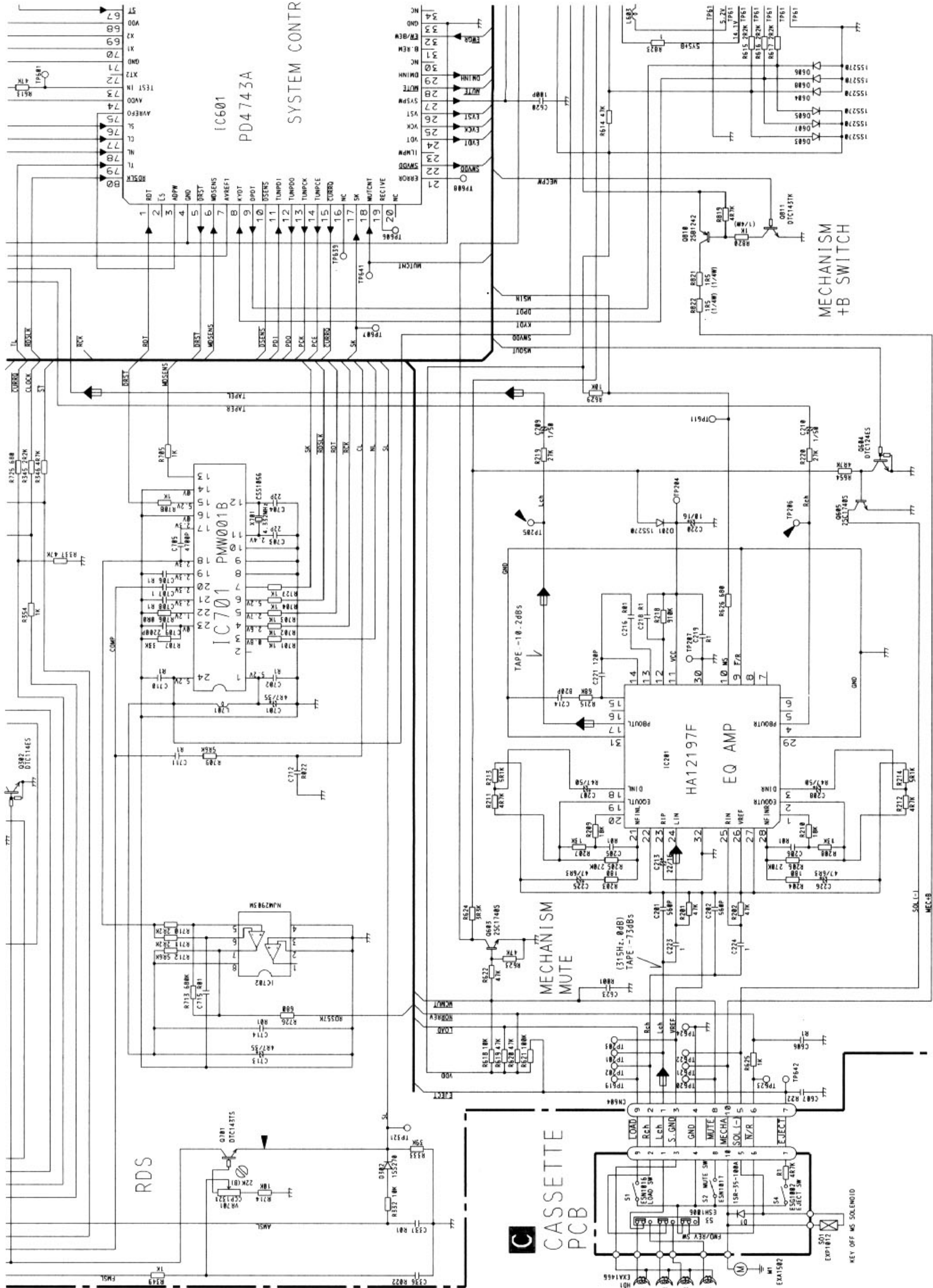
A-a A-b

A TUNER AMP UNIT

B FM/AM TUNER UNIT

- ▶ TAPE SIGNAL
- ◀ FM/MW/LW SIGNAL
- ◀ CD SIGNAL
- ▶ TAPE, FM/MW/LW, CD SIGNAL





A-a A-b

Fig. 6

A-a

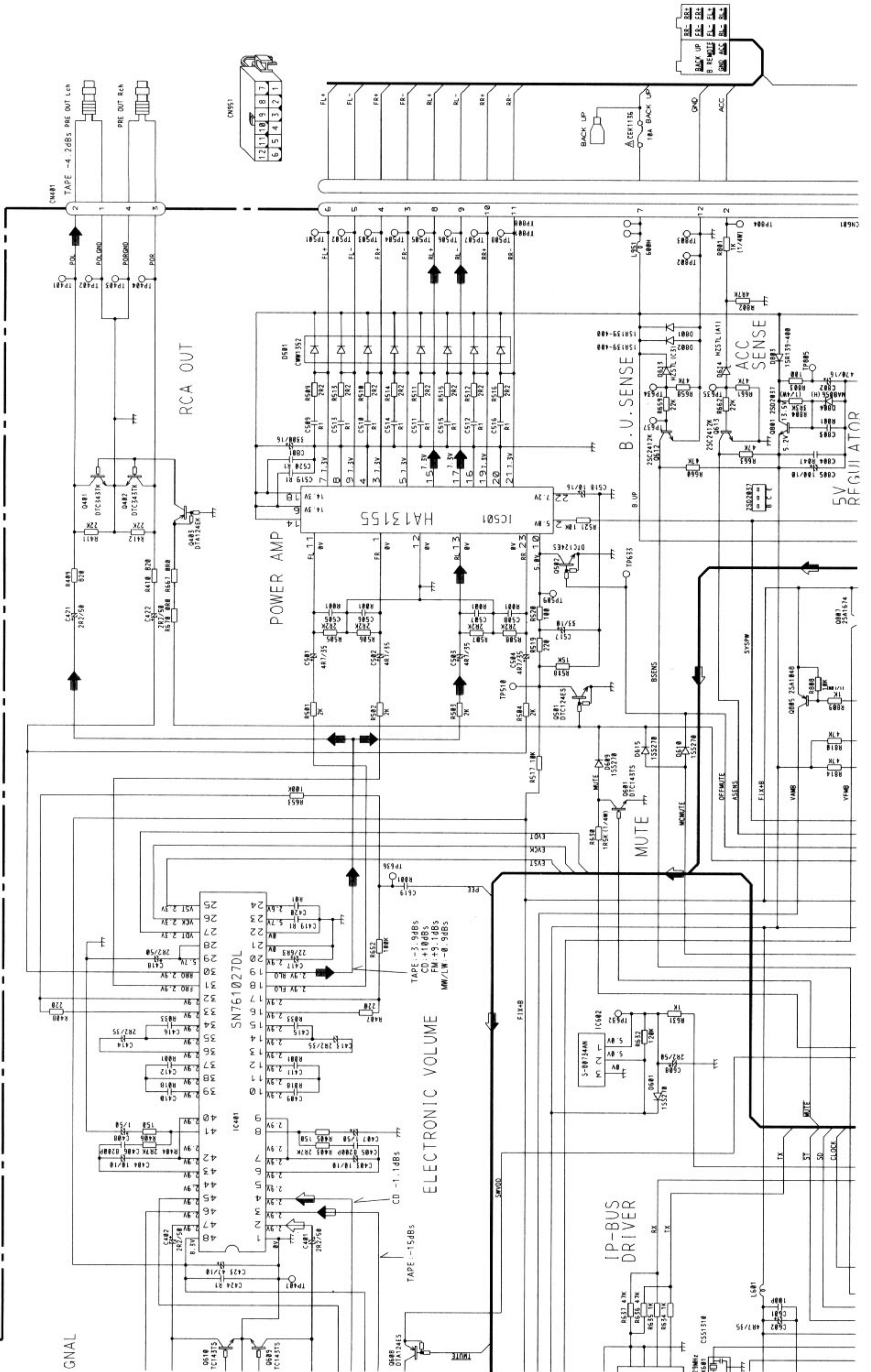
A-a A-b

NOTE:

- ⊠ Symbol indicates a resistor. No differentiation is made between chip resistors and discrete resistors.
- ⊡ Symbol indicates a capacitor. No differentiation is made between chip capacitors and discrete capacitors.

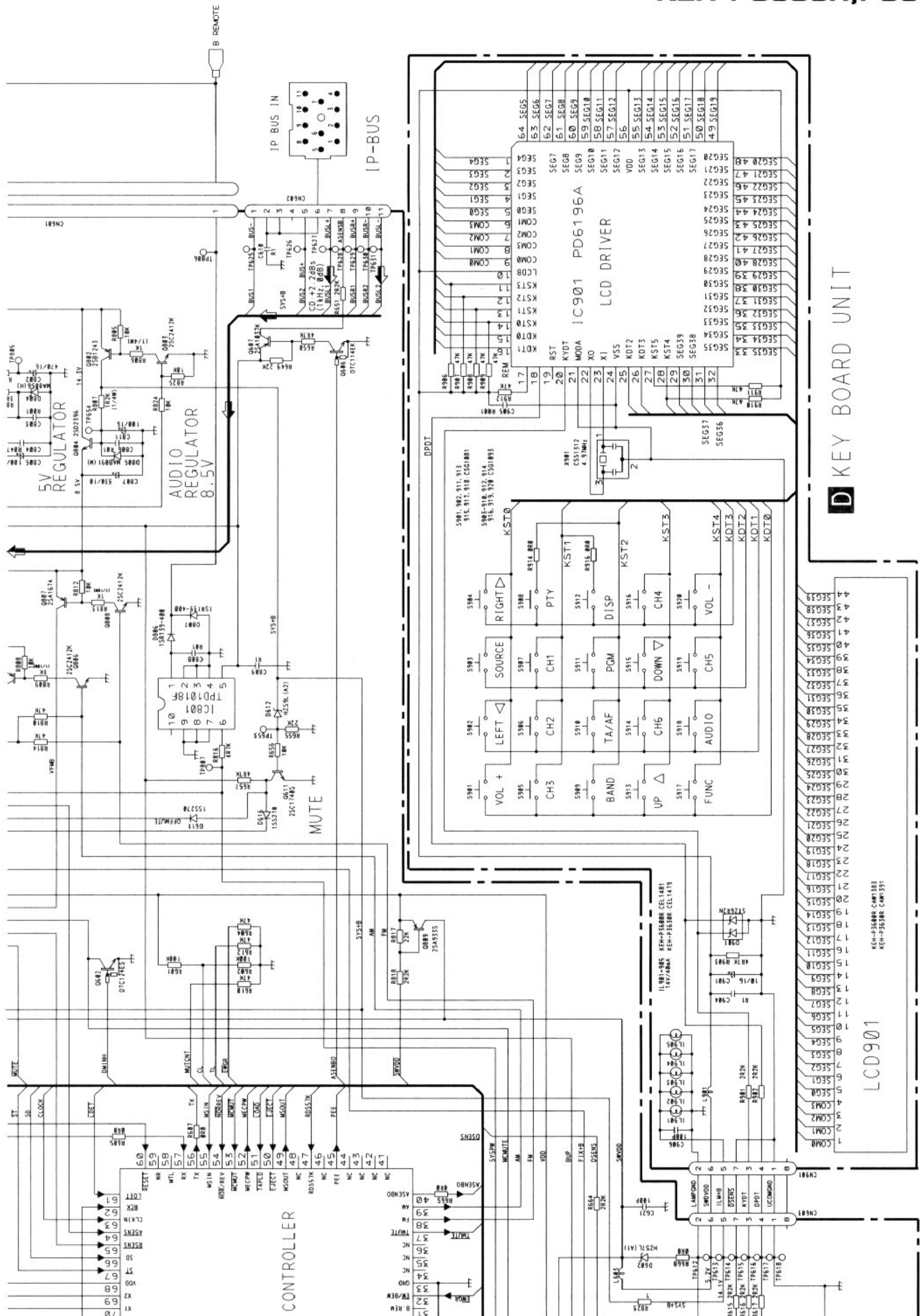
- Decimal points for resistor and capacitor fixed values are expressed as 2.2-2R2 0.022-0R022

The Δ mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.



A-b

A-a A-b



D KEY BOARD UNIT

LCD901

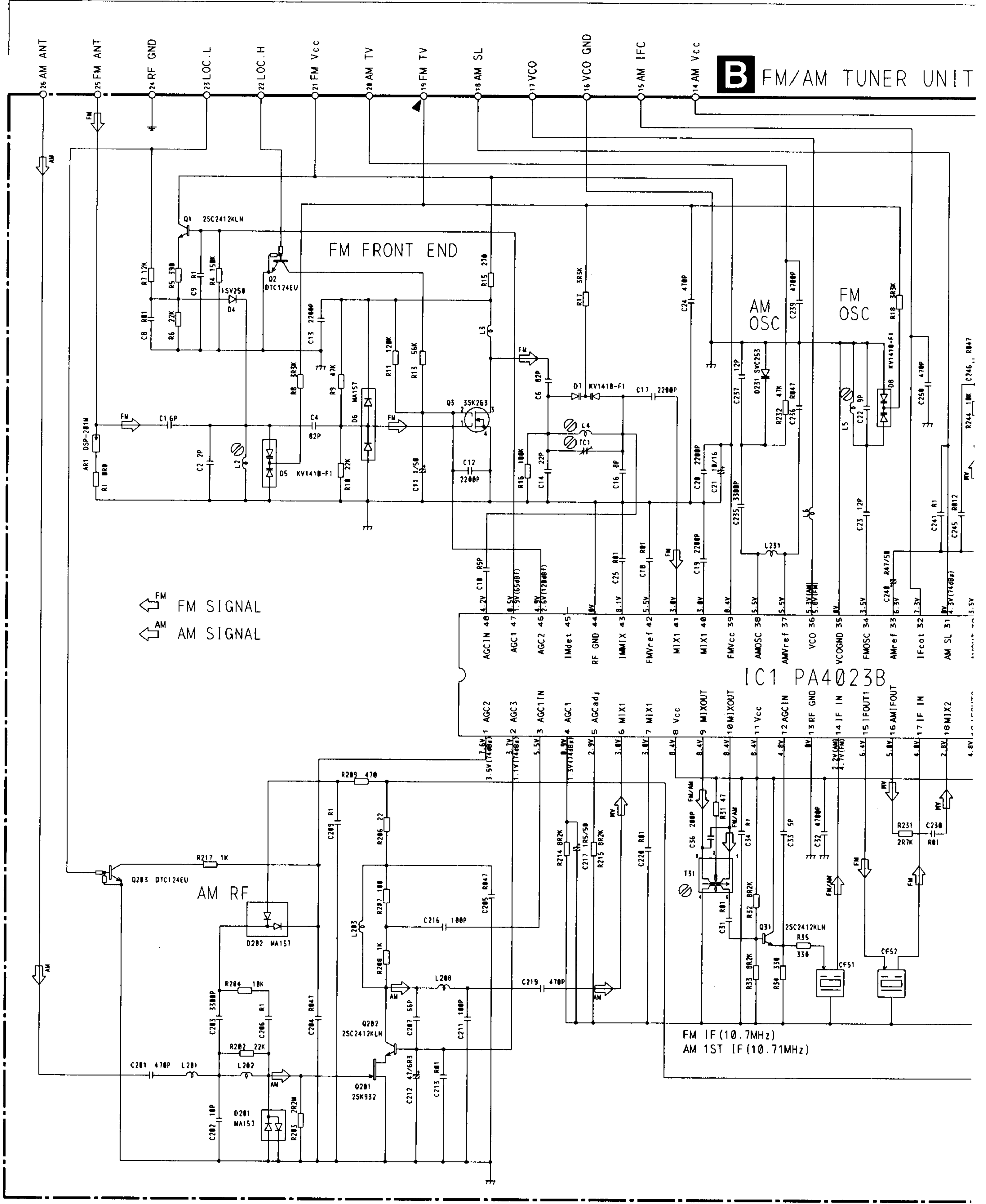
Fig. 7

A-b

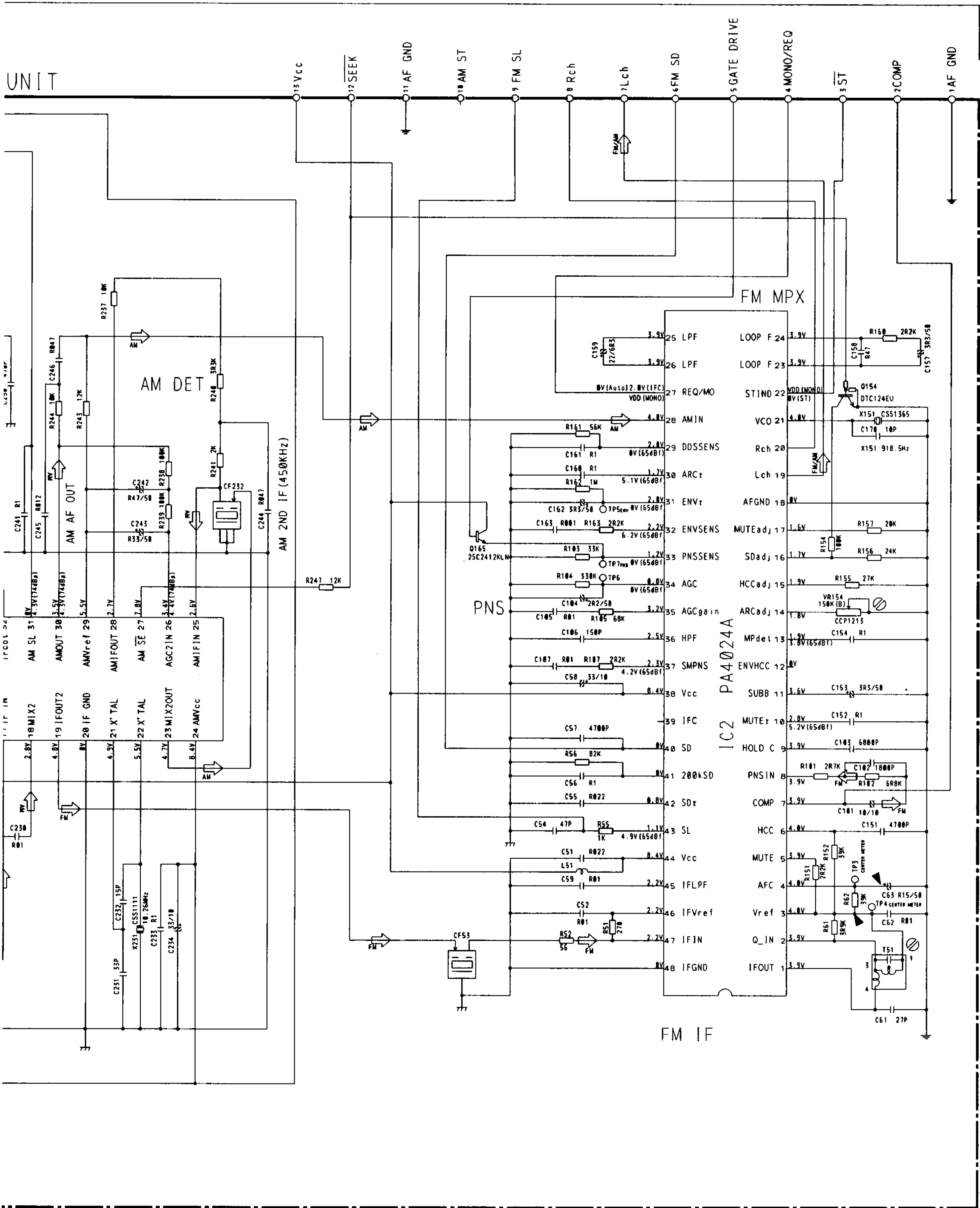
3.2 FM/AM TUNER UNIT

A

B FM/AM TUNER UNIT



B

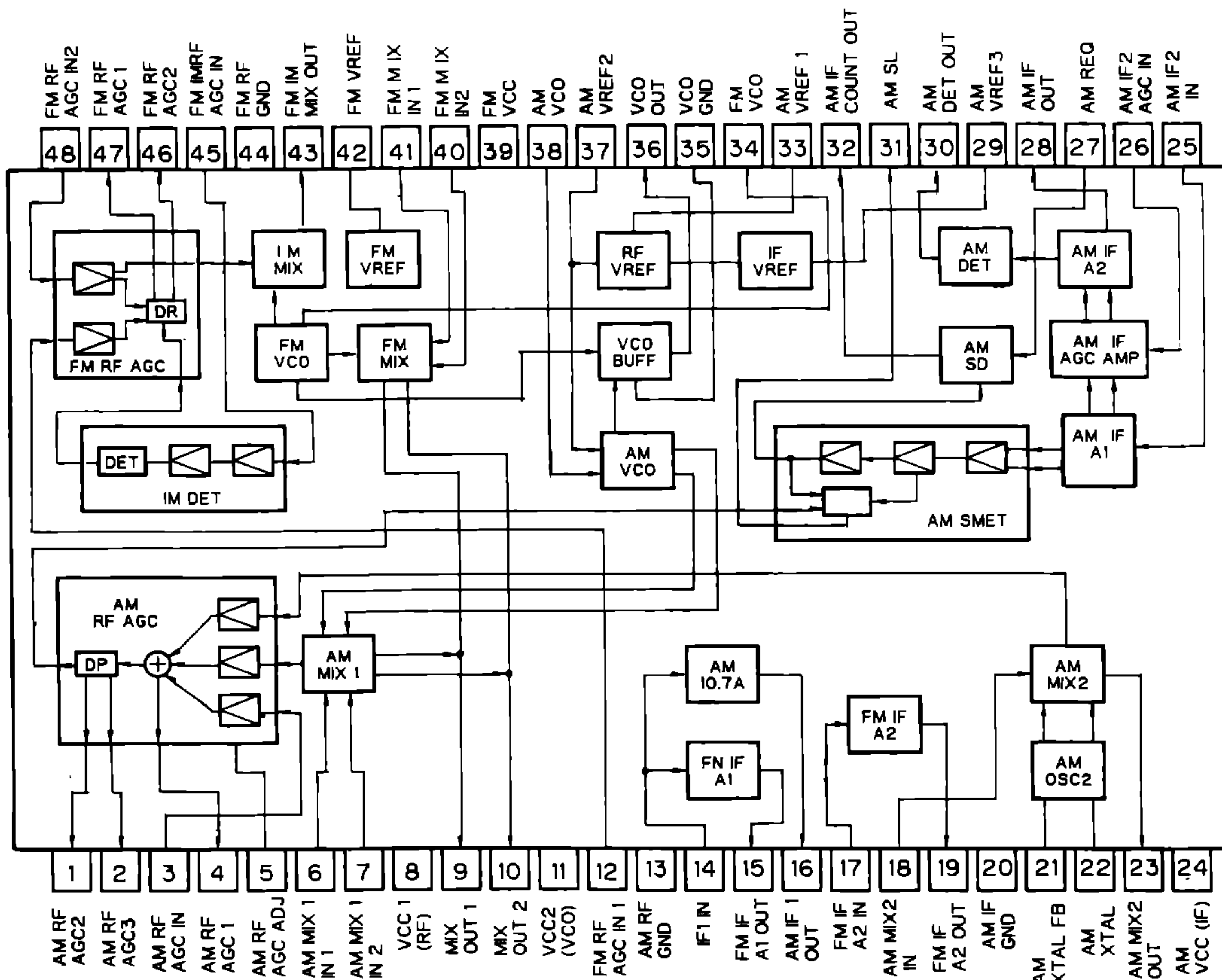


7. GENERAL INFORMATION

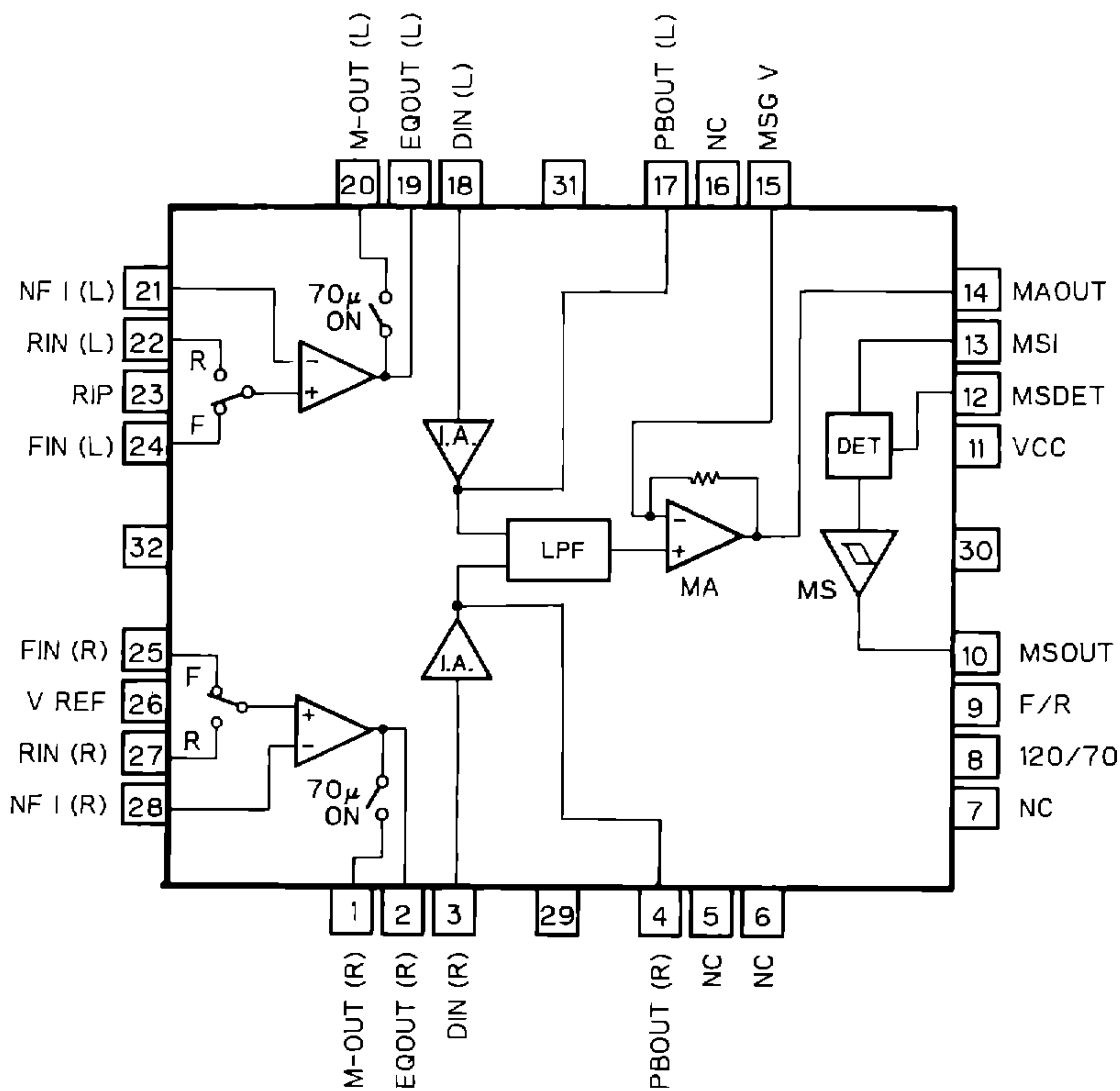
7.1 PARTS

7.1.1 IC

PA4023B

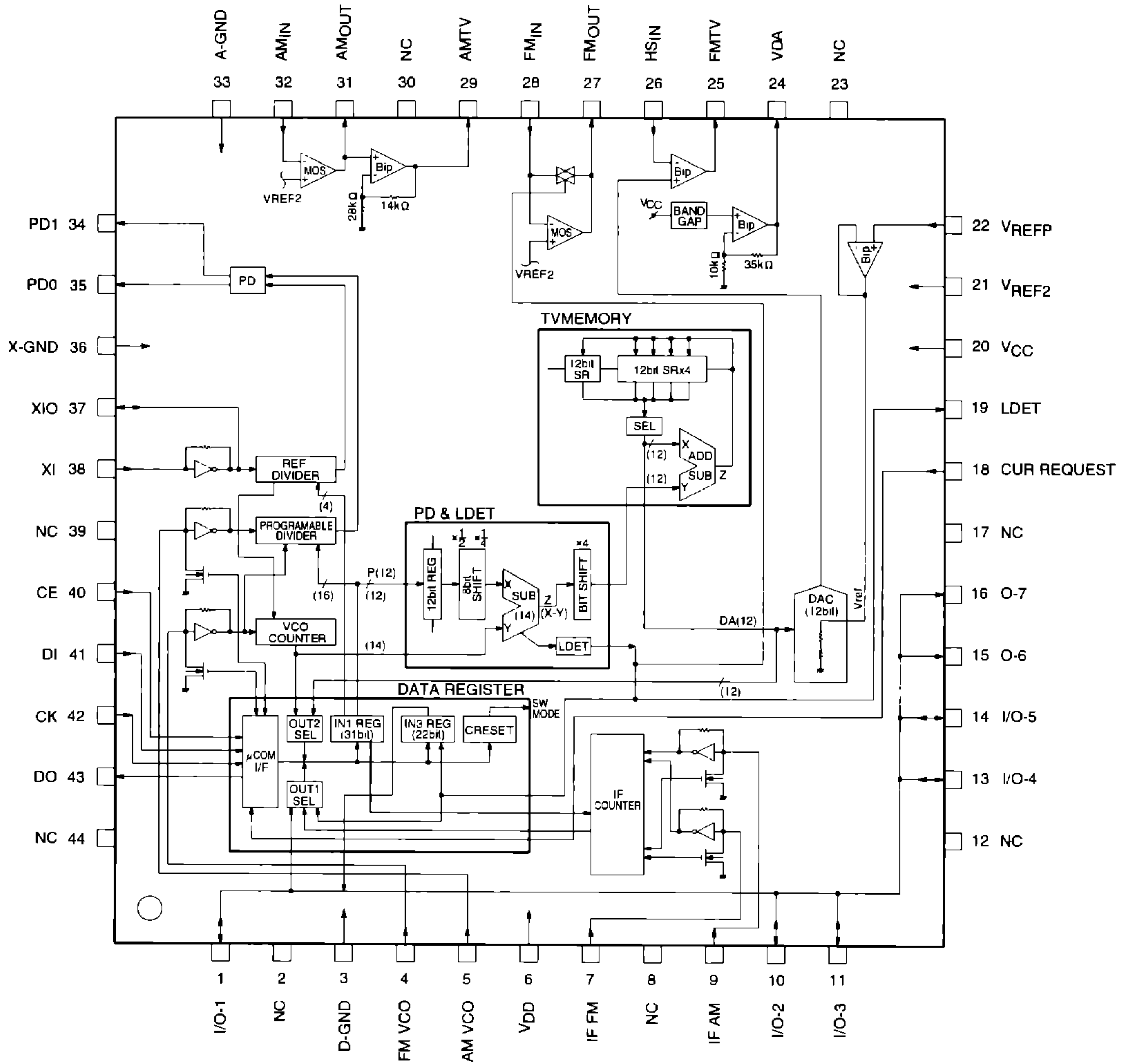


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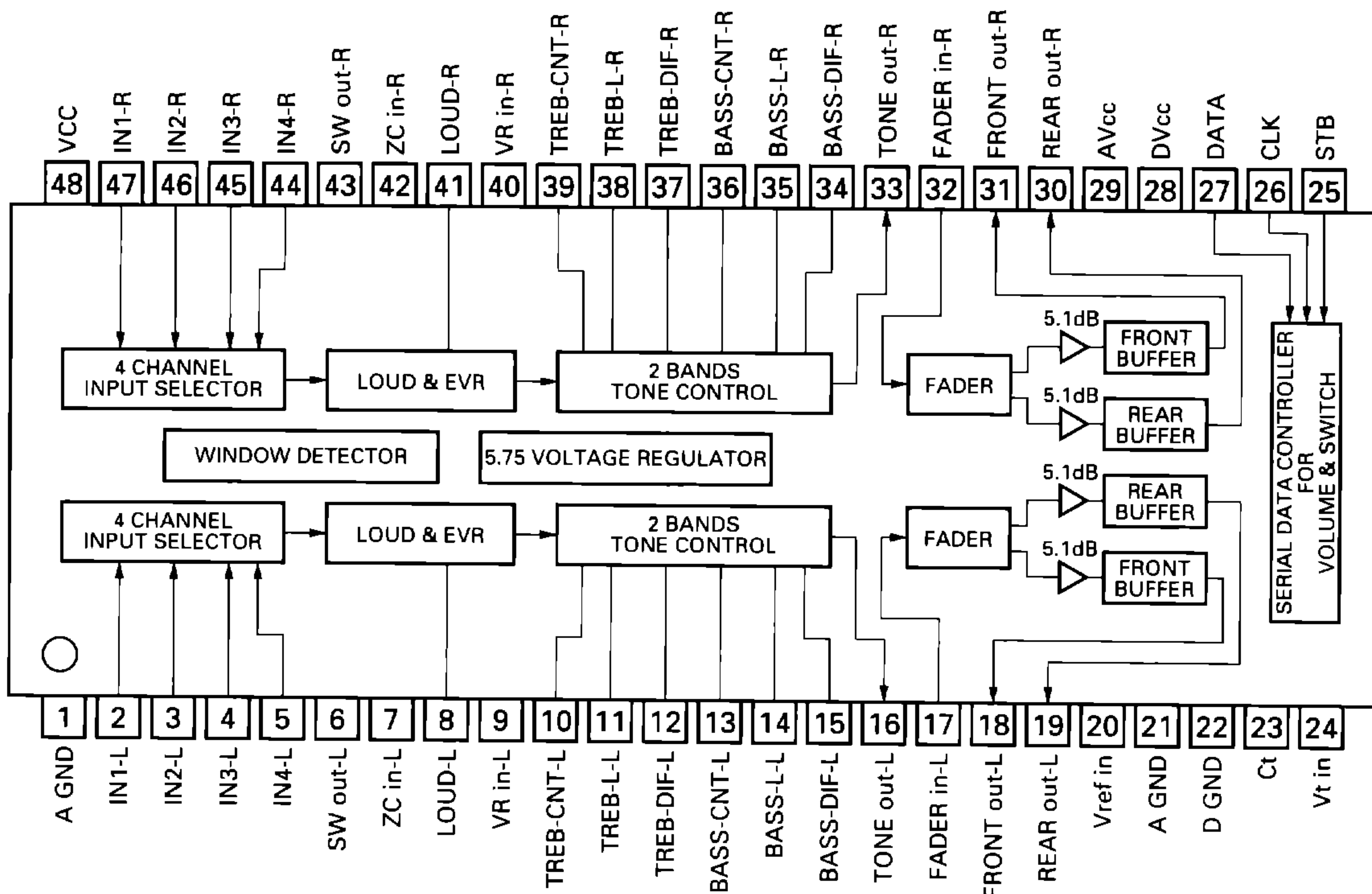


KEH-P3600R,P3630R

PM2005B



SN761027DL



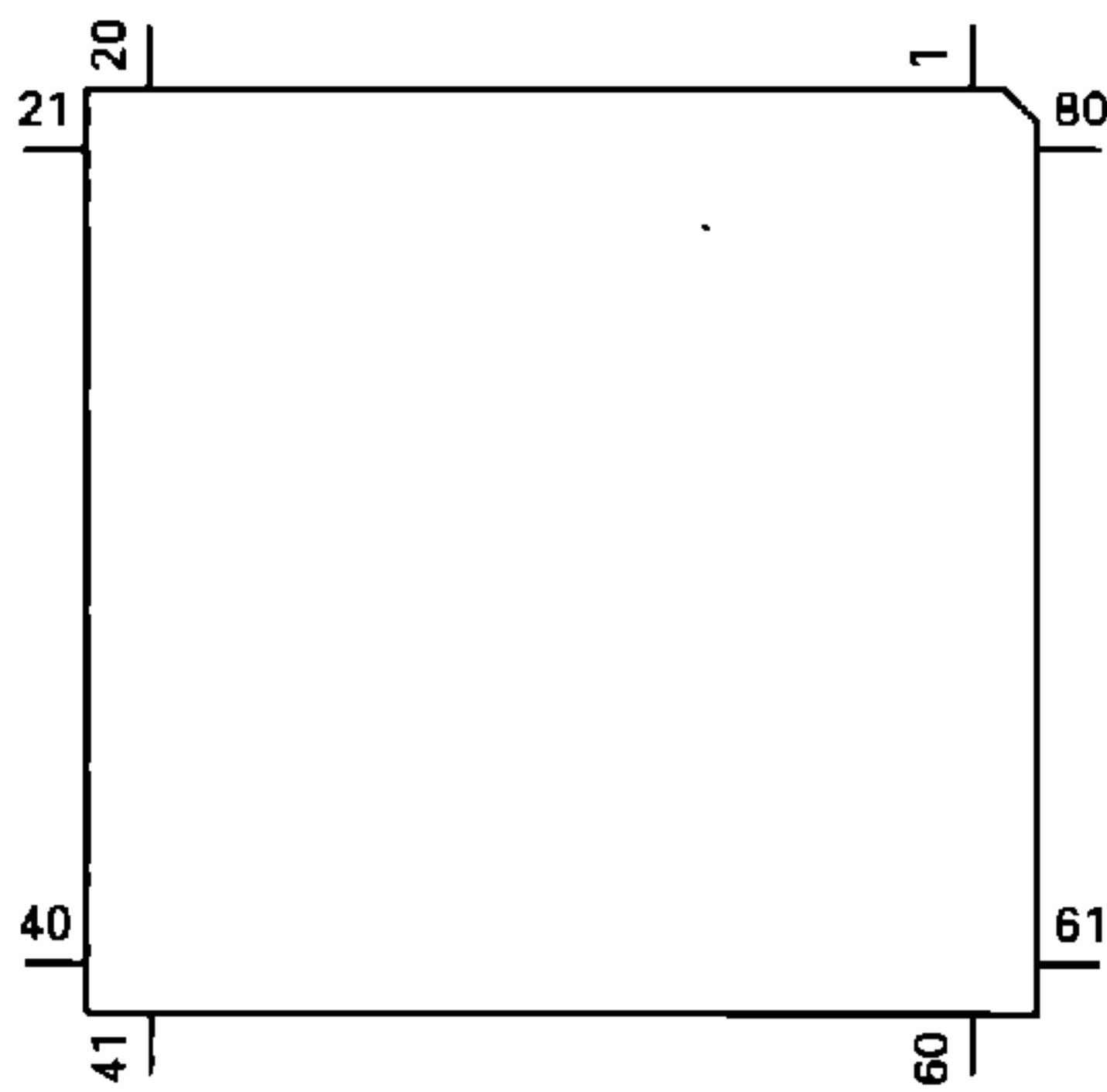
● Pin Functions (PD4744A)

Pin No.	Pin Name	I/O	Format	Function and Operation
1	RDT	I		RDS demodulation data input
2	$\overline{L/S}$	O	C	Sensitivity of noise level select
3	ADPW	O	C	Control output for analog input reference power
4	GND			GND
5	\overline{DRST}	O	C	Decoder reset output
6	MDSENS	I		Modulation detect input
7	AVREF1			(D/A converter standard voltage)
8	KYDT	I		Key data input
9	DPDT	O	C	Key data output
10	\overline{DSENS}	I		Grille detach sense
11	TUNPDI	I		PLL IC data input
12	TUNPDO	O	C	PLL IC data output
13	TUNPCK	O	C	PLL IC clock
14	TUNPCE	O	C	PLL IC chip enable
15	\overline{CURRQ}	O	C	Tuner voltage FIX output
16	NC			Not used
17	SK	I		SK signal input
18	MUTCNT	I		NF mute control input
19	RECIVE	O	C	During RDS data reception output
20	NC			Not used
21	EORR	O	C	Correct RDS error output
22	\overline{SWVDD}	O	C	Grille power supply control output
23	ILMPW	O	C	Illumination power supply control output
24	VDT	O	C	Data output for electronic volume
25	VCK	O	C	Clock output for electronic volume
26	VST	O	C	Strobe pulse output for electronic volume
27	SYSPW	O	C	System power supply control output
28	\overline{MUTE}	O	C	System mute output
29	DMINH	O	C	Mechanism mute cancel output
30	NC			Not used
31	B.REM	O	C	B remote output
32	$\overline{EW/BEW}$	I		Model sense input
33	GND			GND
34-36	NC			Not used
37	\overline{TMUTE}	O	N	Tuner mute output
38	FM	O	N	FM power control output
39	AM	O	N	AM power control output
40	ASENBO	O	C	Slave power supply control output
41-44	NC			Not used
45	PEE	O	C	Beep tone output
46	NC			Not used
47	RDS57K	I		57kHz BP-OUT sense input
48	NC			Not used
49	MSOUT	O	C	MS output
50	\overline{EJECT}	I		Eject key input pin
51	\overline{TAPLD}	I		Tape loading input
52	MECPW	O	C	Mechanism power output
53	MCMUT	I	C	Mechanism mute request
54	$\overline{NOR/REW}$	I		Normal reverse input
55	MSIN	I		MS sense
56	TX	O	C	IP BUS data output
57	RX	I		IP BUS data input
58	MTL	O	C	Metal output

KEH-P3600R,P3630R

Pin No.	Pin Name	I/O	Format	Function and Operation
59	NR	O	C	Dolby output
60	RESET	I		Reset Input
61	LDET	I		PLL lock sense input
62	RCK	I		RDS demodulation clock input
63	CLKIN	I		Clock input
64	ASENS	I		ACC power sense input
65	BSENS	I		Back up power sense input
66	SD	I		SD input
67	ST	I		
68	VDD			Power supply
69	X2			Crystal oscillator connection pin
70	X1			Crystal oscillator connection pin
71	GND			GND
72	XT2			Sub clock
73	TESTIN	I		Test program mode input
74	AVDD			Positive power supply terminal for analog circuit
75	AVREF0			(A/D converter standard voltage input)
76	SL	I		Signal level input
77	CL	I		Synchronizing signal input of display data latch
78	NL	I		Noise level input
79	TL	I		Trigger level input
80	RDSLK	I		RDS LK signal input

*PD4744A

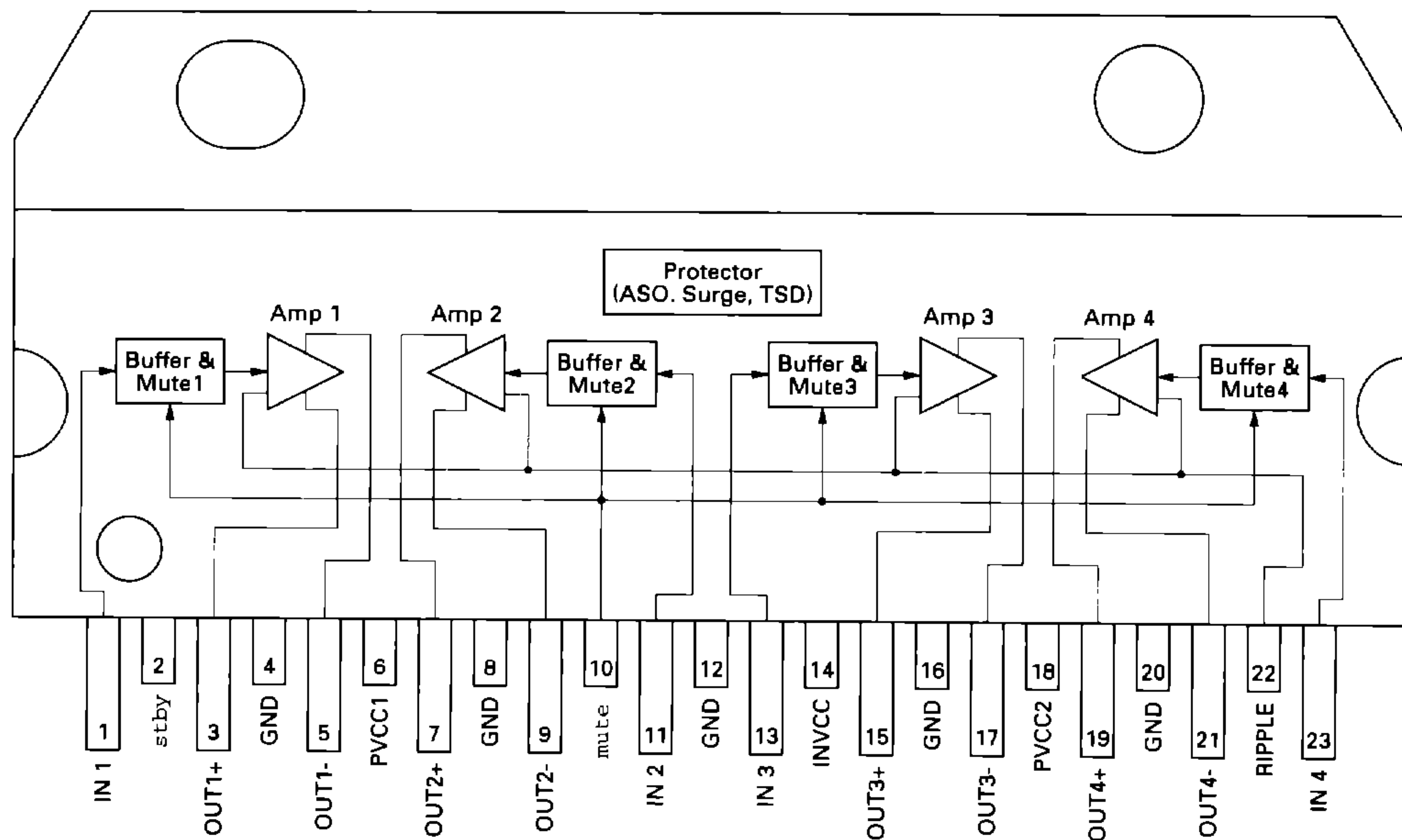


IC's marked by* are MOS type.

Be careful in handling them because they are very liable to be damaged by electrostatic induction.

Format	Meaning
C	C MOS
N	N channel open drain

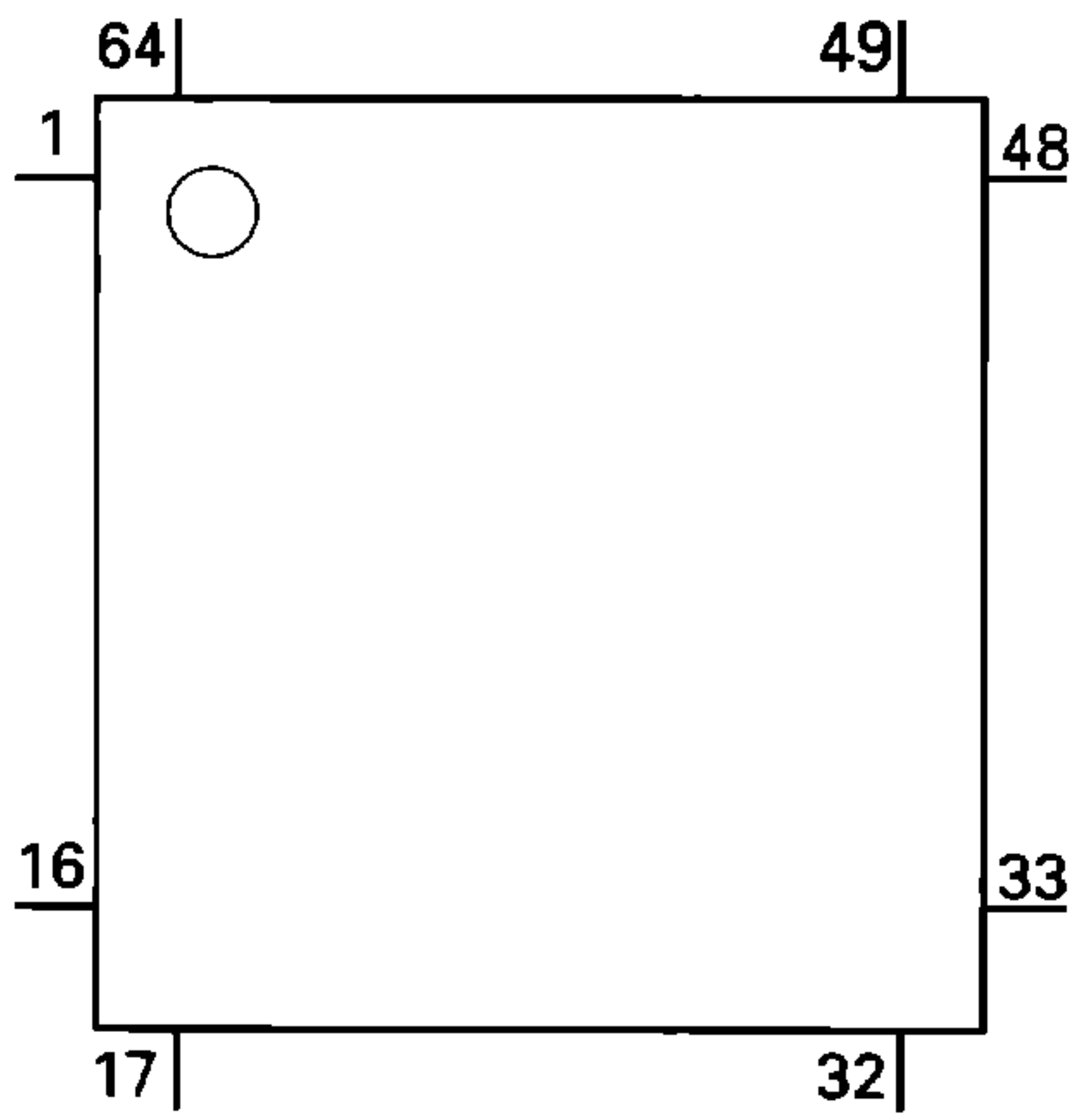
HA13155



● Pin Functions(PD6196A)

Pin No.	Pin Name	I/O	Function and Operation
1-5	SEG4-0	O	LCD segment output
6-9	COM3-0	O	Common driver output
10	V3		LCD bias power supply
11-14	KS4-1	O	Key strobe output
15,16	KD1,2	I	Key data input
17	REM	I	Remote control reception
18	SI	I	UART input
19	RST	I	System reset
20	SO	O	UART output
21	MODA		GND
22	X0		Crystal oscillator connection pin
23	X1		Crystal oscillator connection pin
24	VSS		GND
25,26	KD3,4	I	Key data input
27,28	KS6,5	O	Key strobe output
29-55	SEG39-13	O	LCD segment output
56	VCC		5V
57-64	SEG12-5	O	LCD segment output

*PD6196A

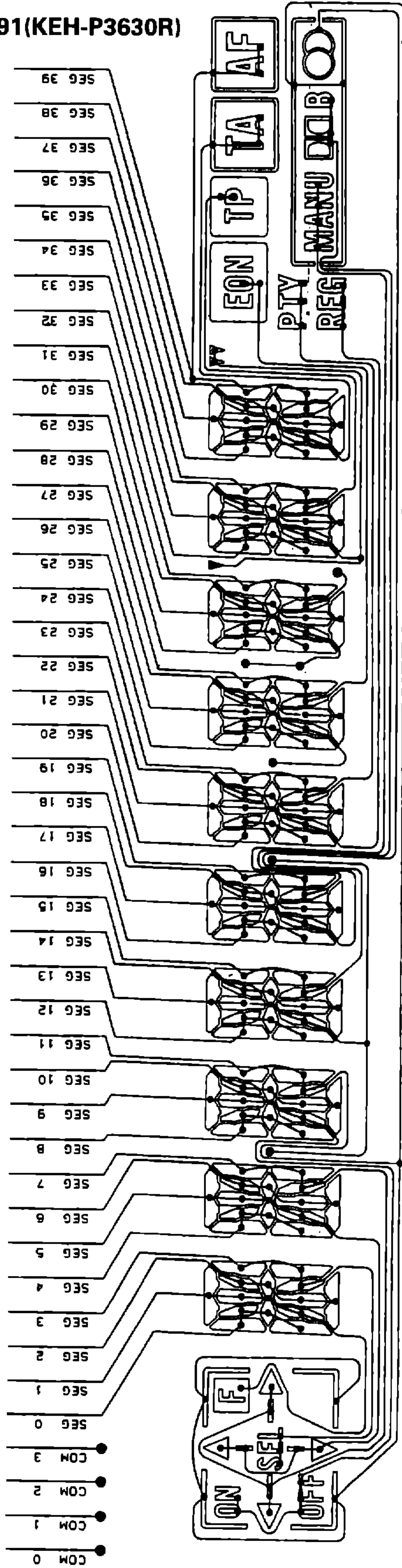


7.1.2 DISPLAY

● CAW1383(KEH-P3600R)

● CAW1391(KEH-P3630R)

SEGMENT



COMMON

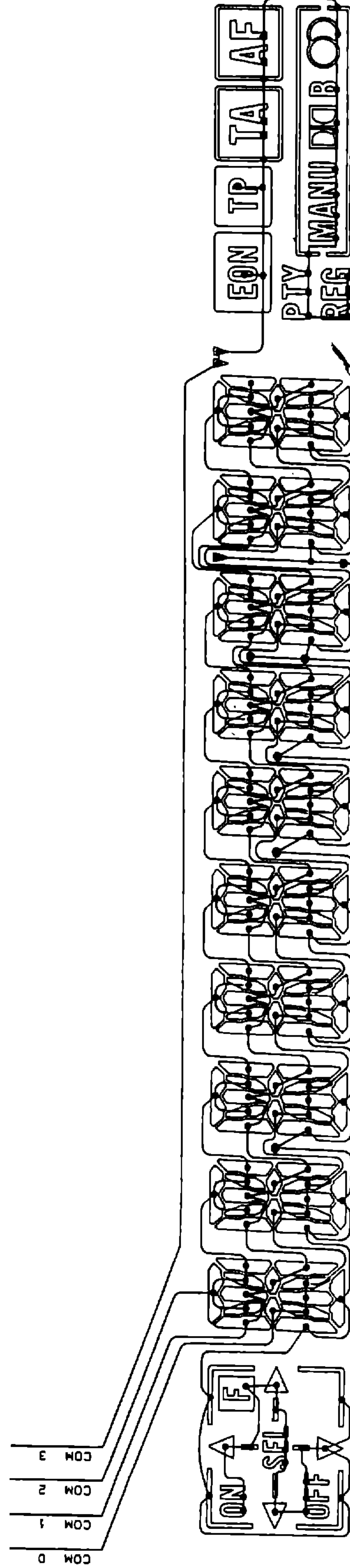
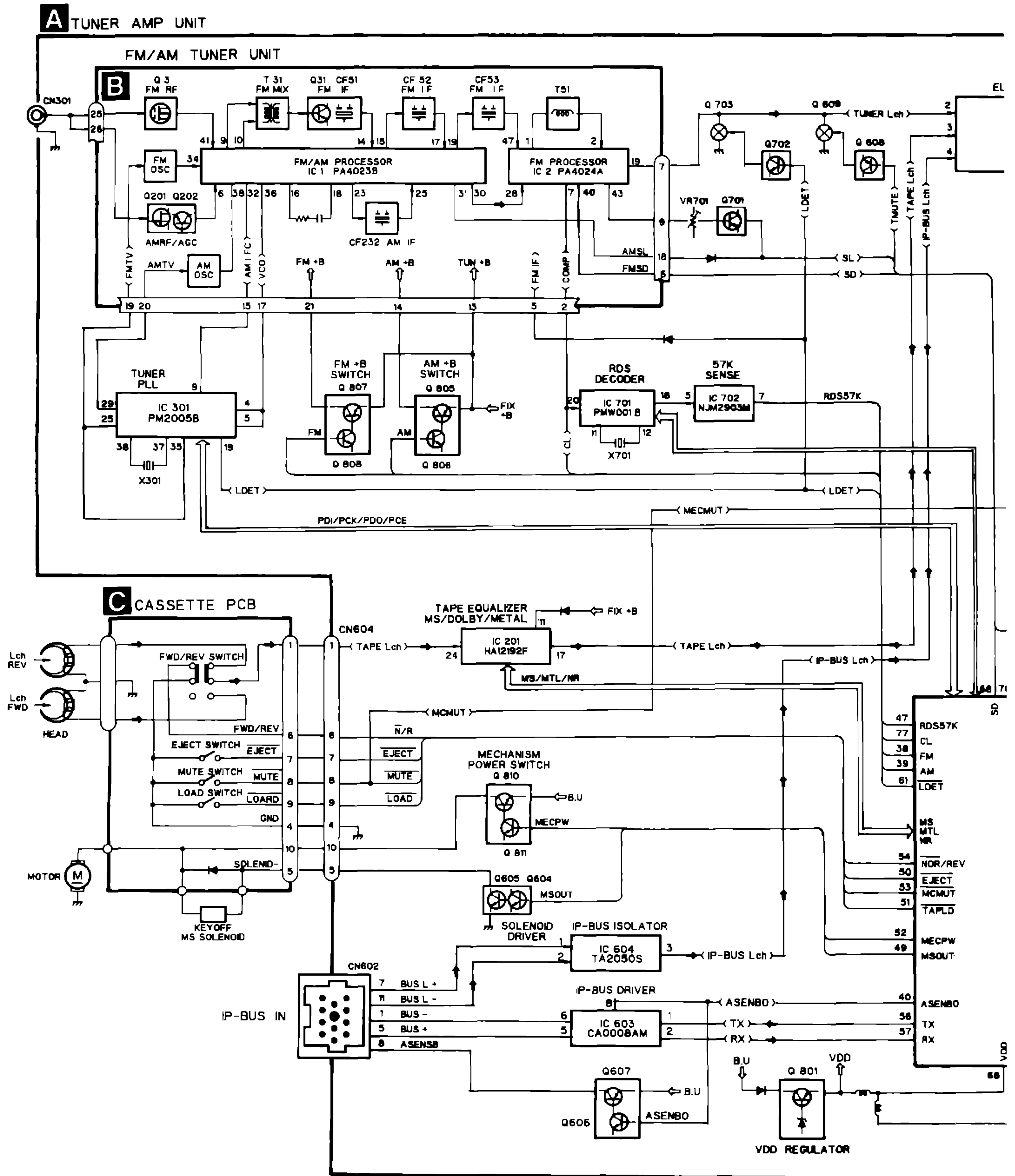


Fig. 17

7.3 EXPLANATION

7.3.1 BLOCK DIAGRAM



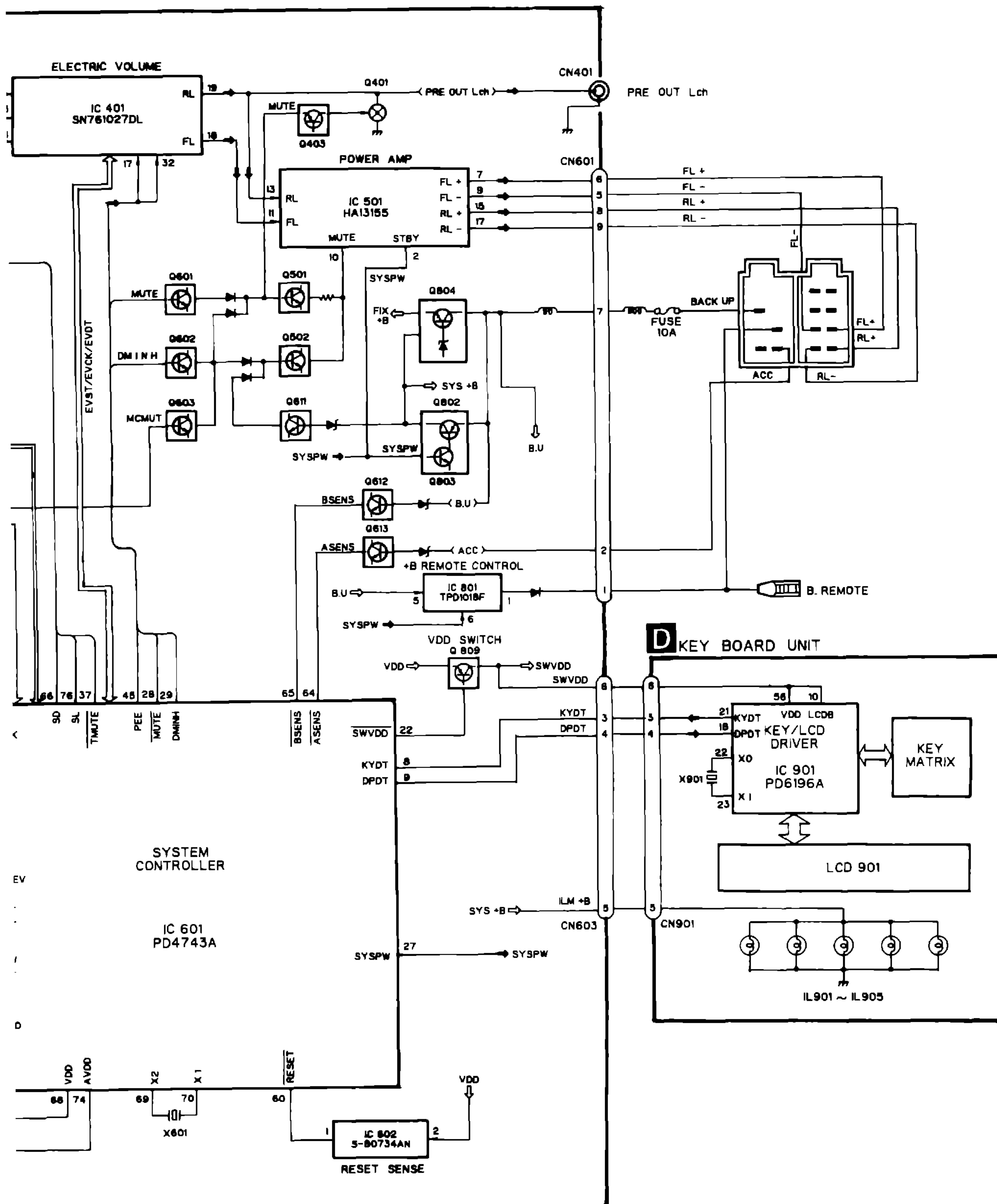


Fig. 22